EE492: End of Semester Reminders

Please refer to the course syllabus and web site for more detailed information.

Thursday 6 December 2007

Design Fair!

<u>8:00-10:00AM</u>: Setup in SUB Ballroom

9:00-9:30AM	Telecom Educational Laboratory	Temperature Control, Laser Diode Arrays
9:30-10:00AM	MicroMouse Team One (BCP)	Wireless Switch Monitor
10:00-10:30AM	MicroMouse Team Two (KHW)	Crankshaft Position
10:30-11:00AM	Doppler Radar	Wireless Link to Welder's Helmet
11:00-11:30AM	Satellite Panel Max Power Transfer	High Voltage, Sawtooth Power Supply
11:30AM-noon	RS-485 Comms Test Unit	Avalanche Dummy Datalogger

Each project team member is required to participate in a demonstration of the *fully functioning project*. In addition to the design fair poster displays, you should be prepared with project goals, schematics, flow diagrams, and similar materials to demonstrate that project performance goals have been met. *Grades will be assessed primarily on the degree to which the project performance goals have been achieved.* Project teams without demonstrable evidence that the design goals have been met should expect a very low score from the review team.

The review team will also assess the team's preparation (documentation ready, readable and sufficient), the ability of each team member to answer project-related questions, and whether or not the appropriate test equipment is available to verify the project's performance.

Noon-6:00PM: Public open house (doors close at 5:30PM, but please plan to be available until 6PM)

The format is a public poster presentation and demonstration of your project. Each student will prepare materials describing his or her part of the project and the project team will display a comprehensive combined poster and demonstrate the project (if possible) during the public session held in the SUB. This is a group presentation with each student participating and describing his or her own contribution to the project.

The entire system hardware and software should be included in the display. The poster materials should include an overview of the project, discussion of engineering constraints, information regarding each student's contribution to the final design, project performance, results, conclusions and recommendations for future work.

NOTE: faculty will be reviewing the poster and display setups primarily during the 3-5PM time slot, although some faculty poster reviews may occur throughout the afternoon.

Friday 7 December 2007

- ECE end-of-semester Blowout party/social, 2-4PM, 6th Floor Cobleigh.

Monday 10 December 2007

- Final self/peer review forms due (to Maher) by 5:00PM
- Technical Documentation Package due (to faculty advisor) by 5:00PM.

Each project team is to produce a Technical Documentation Package for the project. One copy is delivered to the project's faculty advisor and/or sponsor (if required) by 5PM. The goal of the technical documentation package is to supply sufficient information so that an engineer (or follow-on project students) can completely understand and reconstruct your project hardware and software. The package may include, but is not limited to, the following items:

> Schematic diagrams; PC board layout; PC board files; mechanical drawings; construction information; circuit descriptions; written test plans; written test results; software design documentation; software listings; software test plans and results; component data sheets; trouble shooting techniques; error messages; copies of progress reports; references

If a user's manual is appropriate for the project (not all projects will have a user's manual) the project team is to produce one as part of the final documentation. It is to be delivered to the ECE office with the technical documentation package. The faculty project advisor and/or sponsor may require additional copies. The user's manual normally contains complete information for the user to install, run the system, and troubleshoot problems. Maintenance information, if appropriate, should be included, as well as error messages and what to do about them.

Wednesday 12 December 2007

- Complete project documentation package delivered to ECE office by 3:00PM.

Materials to submit include:

1. Engineering Standards and Constraints Review Paper
2. EE492 Design Journals in labeled box. SEE YOUR ADVISOR if you would like to keep your journal.
3. Design Fair presentation package (poster)
4. Technical Documentation Package: Report, Design Description, and User's Manual (if applicable)

and the Senior Design Checklist fully completed.